



U.S. UTILITY Patent Application
O.I.P.E. PATEN PATENT DATE SCANNED HKMD

EXAMINER SUBCLASS ART UNIT CONT/PRIOR CLASS APPLICATION NO. GICENA 2826 257 09/816599

Seong Seo Young Chung Jong Paek Jae Ku

Semiconductor package including stacked chips
.

PTO-2040 12/99

ISSUING CLASSIFICATION										
ORIGINAL			CROSS REFERENCE(S)							
CLASS	SUBCLASS	CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)							
						1.4				
NTERNATIONAL CLASSIFICATION		1								
TERRATION	CE OEAGGII TOATTO	1				43.0				
		+								
-+		+								
<del>                                     </del>		+								
			<del></del> -	Continued on Issue Slip Inside File Jacket						

TERMINAL		DRAWINGS	CLAIMS ALLOWED		
DISCLAIMER	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
☐ The term of this patent				NOTICE OF ALLOWANCE MAILED	
subsequent to (date) has been disclaimed.	(Assistant Examiner)		(Date)	·	
The term of this patent shall not extend beyond the expiration date of U.S Patent. No.				ISS	SUE FEE
				Amount Due	Date Paid
	(Primary	Examiner)	(Date)		
The terminalmonths of		•		ISSUE BA	ATCH NUMBER
this patent have been disclaimed.	(Legal Instrur	nents Examiner)	(Date)		
WARNING: The information disclosed herein may be re Possession outside the U.S. Patent & Trade	stricted. Unauthorized	d disclosure may be d to authorized em	prohibited by the ployees and contra	United States Code Title ctors only.	
Form PTO-436A		-	FILED WITH	_	FICHE CD-R

Form PTO-436A (Rev. 6/99)

(Attached in pocket on right Inside flap)